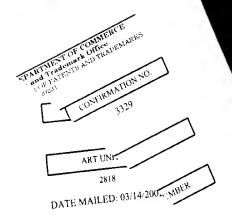


## United States Patent and Trademark

· Commonwell		FIRST NAMED INVENTO
APPLICATION NO.	FILING DATE	Xiaowei Deng
09/976,983	10/12/2001	

03/14/2002

TEXAS INSTRUMENTS INCORPORATED P O BOX 655474, M/S 3999 DALLAS, TX 75265



Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	1	DENG ET AL.	
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of the above, ciam			is/are rejected.
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Page 2 Application/Control Number: 09/976,983 Art Unit: 2818 Claim Rejections - 35 USC § 112 The following is a quotation of the first paragraph of 35 U.S.C. 112: 1. The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention. 2. Claims 1-2 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claims 1-2 are not clearly understood because of the following reasons: a) It is not clear how the voltage applied between the gate and the source/drain terminals of the PMOS drive transistors and the voltage applied between the gate and the source/drain terminals the NMOS pass transistors can be the same as described in lines 19-24, page 3; lines 32-33, page 5; lines 1-10, page 6 and in last paragraph of claim 1. Since Fig. 3 of the present invention does show that the gates of NMOS pass transistor 150 and 160 are connected to the word line 210, while, the gates of the PMOS drive transistors 180 and 170 are, respectively, connected to output nodes 220 and 230; and b) The limitation as recited in claim 2 is not described in the specification.

Art Unit: 2818

## Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claims 1-2 are, insofar as understood, rejected under 35 U.S.C. 102(a) as being anticipated by Portacci, 6,172,901.

Portacci, 6,172,901, discloses in Fig. 1 Prior Art a SRAM cell comprising: PMOS drive transistors 20 and 30;

NMOS pass transistors 26 and 36;

bit lines BL and \BL;

word line WL; wherein: the current flowing through each of the PMOS drive transistors 20 and 30 is less than the current flowing through each of the NMOS pass transistors 26 and 36 (see lines 23-30, column 7).

## Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Marr et al., 6,172,899, Kim, 5,684,735, and Walker et al., 4,320,312.

Application/Control Number: 09/976,983 Page 4 Art Unit: 2818 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TRONG PHAN whose telephone number is (703) 308-4870 and email address is trong.phan@uspto.gov than trong PRIMARY EXAMINER March 4, 2002